

**Remarks:**

Applicants have read and considered the Office Action dated October 9, 2009. Claim 15 has been amended. Claims 1-16 are currently pending. Reconsideration is hereby requested.

The Specification has been amended to clarify a minor typographical error to correct two occurrences of “comparator 8” that are now correctly recited as “adder 8.” The Specification is now consistent throughout. Applicants assert that no new matter has been added.

In the Action, the drawings were objected to as the adder in Figure 1 is shown as having three inputs with one being indefinite as to the source and intent. Applicants assert that the drawings only show two inputs and that the third line is actually the lead line for the reference numeral 8. Figure 1 has been amended to more clearly show that the third line is actually a lead line to reference numeral 8. In addition, the drawings use the word “Figur” rather than the correct English spelling. The drawings have been amended to correctly recite “Figure” throughout. Applicants assert that the new formal drawings overcome the objection to the drawings and request that the objection be withdrawn.

Claims 15 and 16 were rejected under 35 U.S.C. § 112, second paragraph, as being indefinite. The Action states that claim 15 indicates that control signal generation circuitry includes not only an adder for the feedforward or feedback control signals but also a digital to analog converter. The Office Action indicates that three inputs are claimed going to the adder. The Action states that in Figure 1 there are only two labeled inputs to the adder and the third input is indefinite. As discussed above, the third line in Figure 1 is actually a lead line to reference numeral 8. As discussed above, the drawings have been corrected to show that there

are only two inputs. Moreover, claim 15 has been amended to clarify that the circuitry comprises a digital to analog converter and an adder. Claim 15 now clearly recites that the adder adds the feedback control signal and a feedforward control signal. Claim 15 clarifies that the digital to analog converter is not added by the adder. Applicants assert that claim 15 clearly recites the control circuit and overcomes the indefiniteness rejection. Applicants request that the rejection under 35 U.S.C. § 112, second paragraph, be withdrawn.

Claims 1-14 were allowed. Applicants thank the Examiner for the allowance of these claims. In addition, claims 15 and 16 were indicated as being allowable if rewritten to overcome the rejections under 35 U.S.C. § 112, second paragraph, and to include all of the limitations of the base claim and any intervening claims. Applicants thank the Examiner for the indication of allowable subject matter. Claim 15 has been amended as discussed above and is believed to overcome the rejections under 35 U.S.C. § 112, second paragraph. Therefore, Applicants assert that claims 15 and 16 are in condition for allowance. Applicants assert that all claims are therefore allowable.

A speedy and favorable action in the form of a Notice of Allowance is hereby solicited. If the Examiner feels that a telephone interview may be helpful in this matter, please contact Applicant's representative at (612) 336-4728.

U.S. Patent Application Serial No. 10/568,139  
Reply to Office Action dated October 9, 2009

Please consider this a PETITION FOR EXTENSION OF TIME for a sufficient number of months to enter these papers or any future reply, if appropriate. Please charge any additional fees or credit overpayment to Deposit Account No. 13-2725.



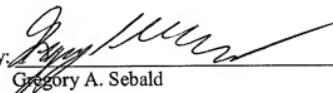
Dated:

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Respectfully submitted,

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